

## **Electrical and Optical Characterization of Semiconductors**

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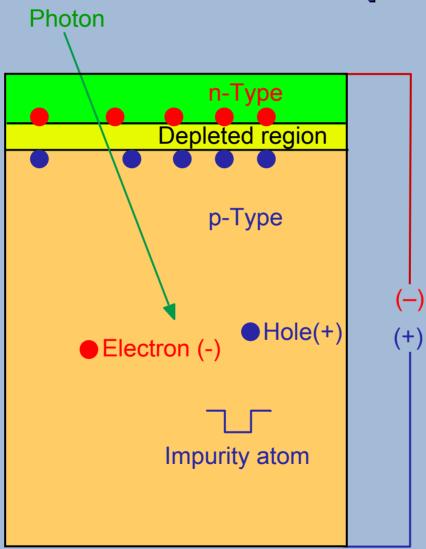
## Electro-Optical Characterization Team R. K. Ahrenkiel: Team Leader and Research Fellow

- Recombination Lifetime Characterization
- Photoluminescence Spectroscopy
- Deep Level Transient Spectroscopy (DLTS)
- Fourier Transform Infrared Spectroscopy
- Scanning Ellipsometry
- Technique Development

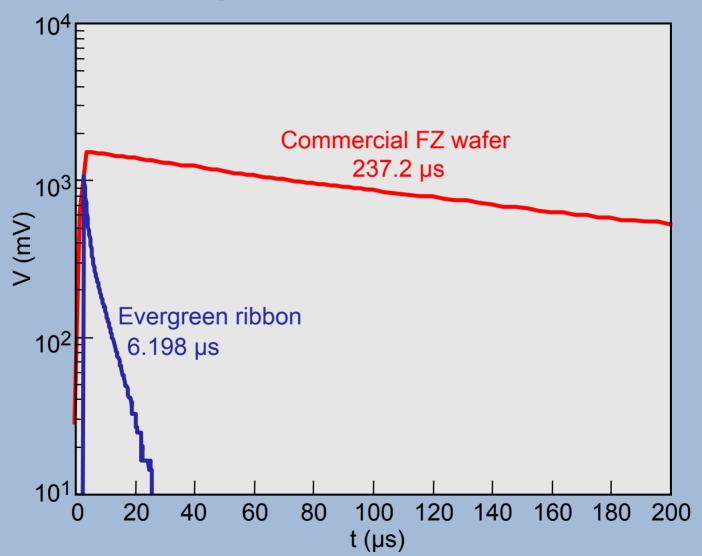
### Electro-Optical Characterization Team R. K. Ahrenkiel: Team Leader and Research Fellow

- R. K. Ahrenkiel: Photoconductive Lifetime (RCPCD)
- Pat Dippo: Energy Resolved Photoluminescence
- Brian Keyes: Fourier Transform Spectroscopy
- Dean Levi: Ellipsometry
- Bhushan Sopori: Technique Development
- Wyatt Metzger: Photoluminescence Lifetime and Device Modeling
- Steve Johnston: Deep Level Transient Spectroscopy
- Lynn Gedvilas: Fourier Transform Spectroscopy
- Four Graduate Students: J. Dashdorj and J. Luther (CSM), Sung Ho Han (CU-Boulder), Chuan Li (New Jersey Institute of Technology)
- Sabbatical: (9/04) Prof. Tim Gfroerer

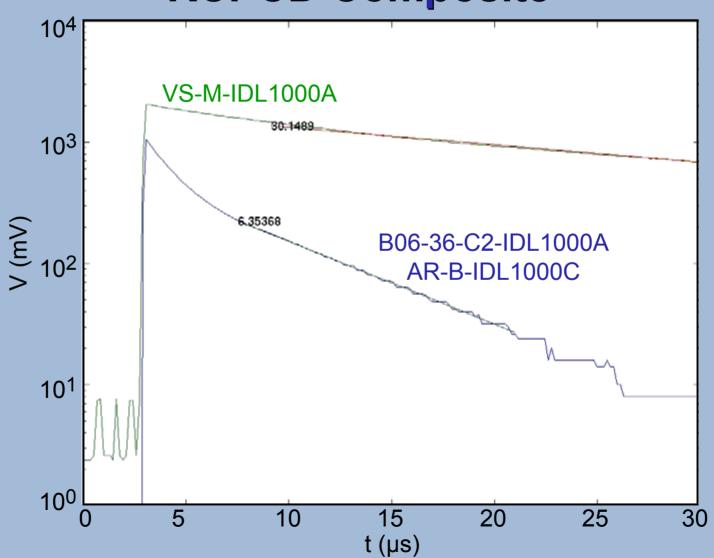
#### **Photovoltaic Principles**

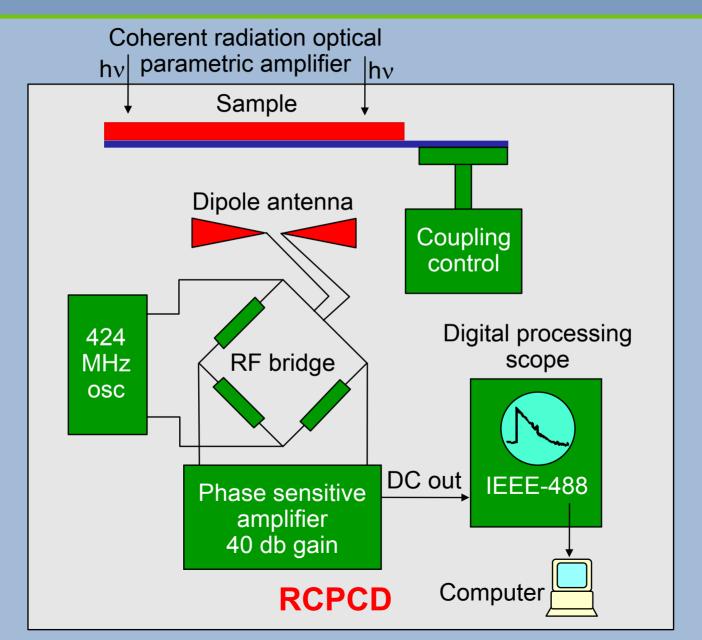


#### **Resonance-coupled Photoconductive Decay**



#### **RCPCD Composite**



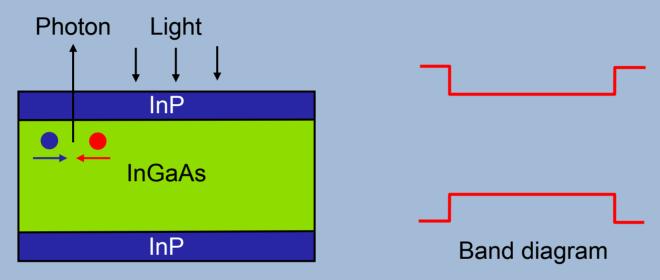


# Time-resolved Photoluminescence and Lifetime Measurements

#### **Time-resolved Photoluminescence**

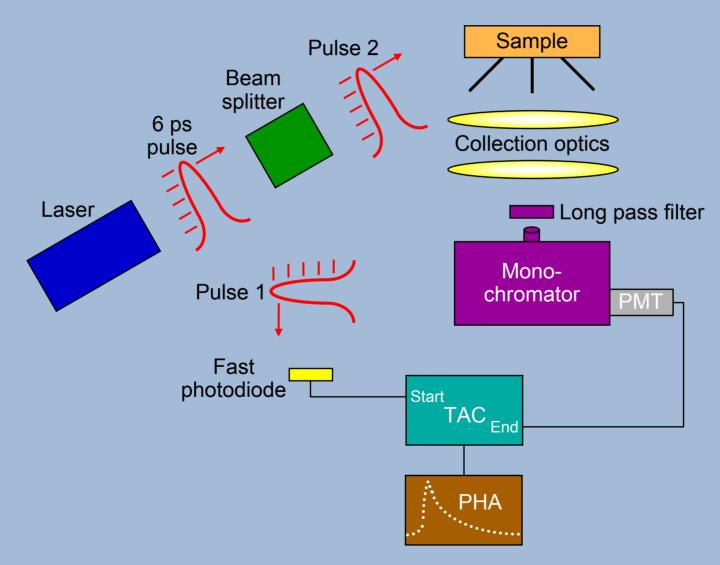
- Inject excess carriers into a sample with laser causing photoluminescence
- Watch the photoluminescence intensity decay
- Use a semiconductor diode, single photon counting, up-conversion TRPL

#### The Sample

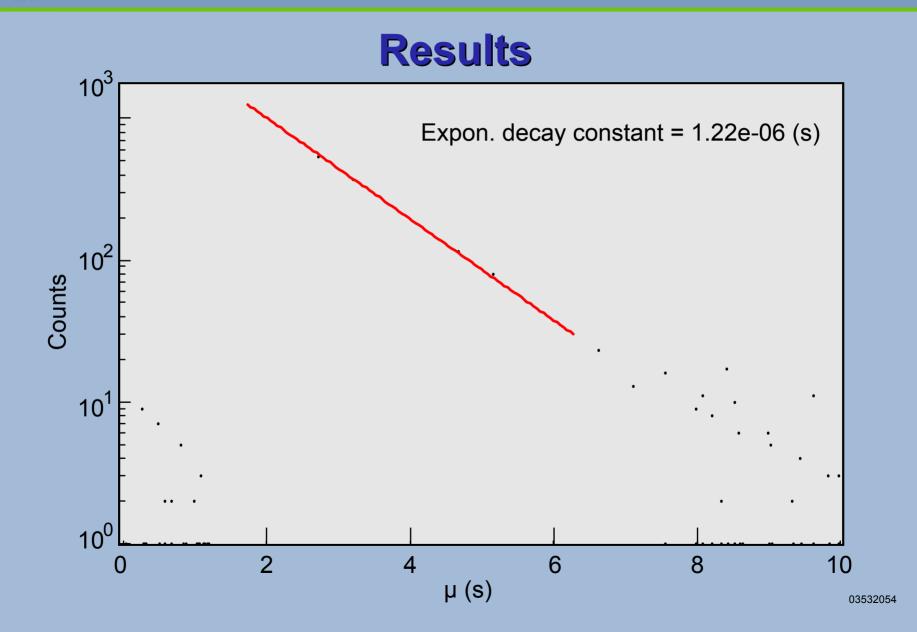


- Double heterostructure confines carriers and provides surface passivation
- Cap layers are generally very thin and transparent to PL and incident laser light
- Not limited to this structure, but preferable

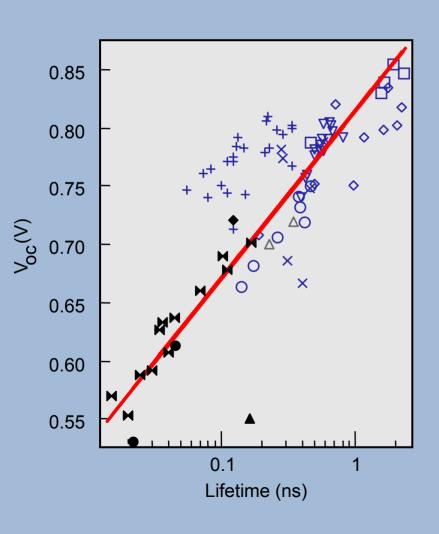
#### **Single Photon Counting Schematic**



- 1. We count a photon once in about 300 attempts.
- We make
   1 million
   attempts
   per second.
- 3. We finish with a histogram of photon counts vs. time.



#### Lifetime often Correlated with Voc



CdTe – *JAP* 94 (5): 3549-3555, Sep. 1, 2003

CIGS – Proceedings of the 29<sup>th</sup> IEEE

pp.511-514, 2002

CIGS - JAP 94 (9): 5584-5591, Nov. 1, 2003

CIGS – APL **73**(9): 1224-1226, 1998

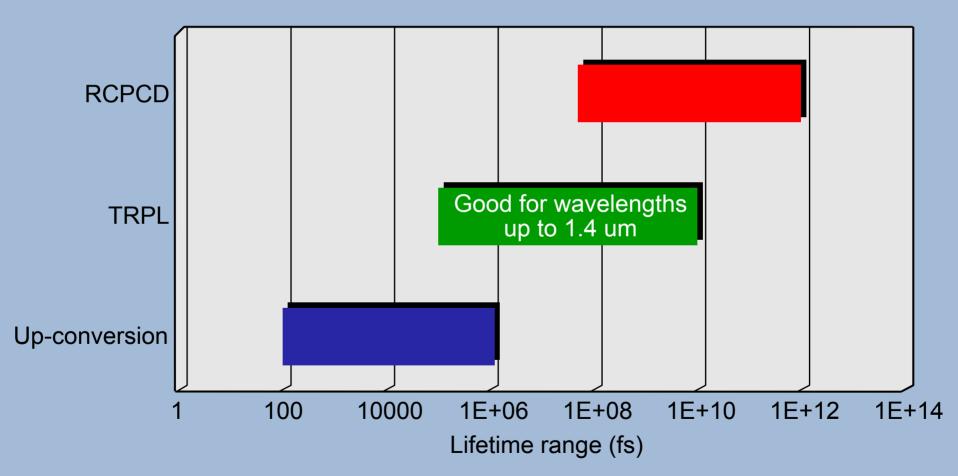
CIGS - Thin Solid Films 387: 262-267, 2001

GalnAs – 3rd World Conference on

Photovoltaic Energy Conversion 2003

GaNP - *J. Cryst. Growth* 259 (3): 223–231, 2003

#### The Experimental Range



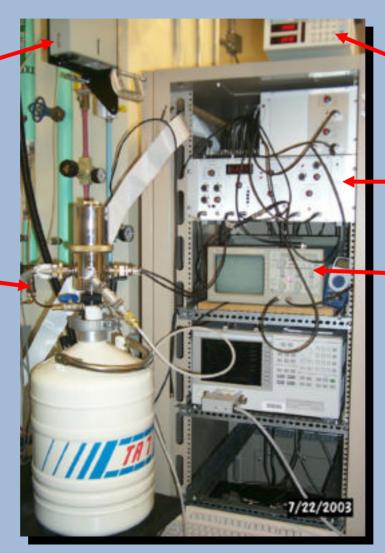
#### **Lifetime Ranges for Different Materials**

| Material | Lifetime Range | Mechanism                | Injection | Control       |
|----------|----------------|--------------------------|-----------|---------------|
| GaAs     | Short - 22 μs  | Radiative, SRH           | yes       | yes           |
| GalnAs   | 100 fs - 10 μs | Auger, Radiative,<br>SRH | yes       | yes           |
| GalnP    | 200 ps - 25 ns | Mostly SRH               | no        | some          |
| CdTe     | 200 ps - 2ns   | nonradiative             | no        | some          |
| CIGS     | 300 ps - 3 ns  | nonradiative             | no        | not monitored |
| GaNP     | 100 ps - 10 ns | nonradiative             | no        | Depends on N  |
| GalnAsN  | 100 ps - 10 ns | nonradiative             | no        | Depends on N  |
| GaAsN    | 100 ps - 10 ns | nonradiative             | no        | Depends on N  |
| GalnN    | 100 ps - 10 ns | nonradiative             | no        | Depends on N  |

#### **Deep Level Transient Spectroscopy**

Laser for option of optical DLTS

Liquid nitrogen dewar

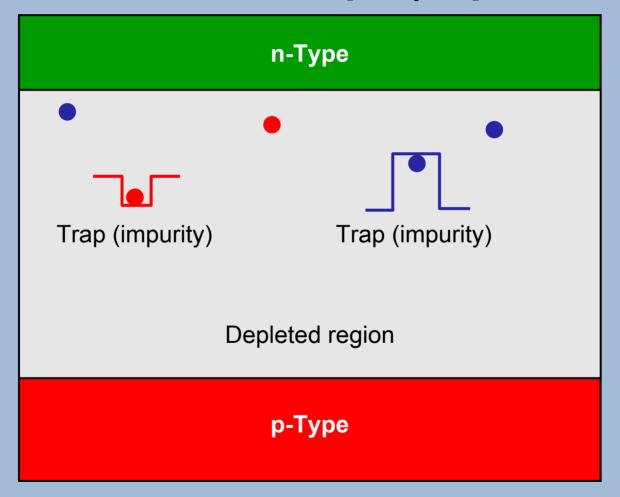


Temperature controller

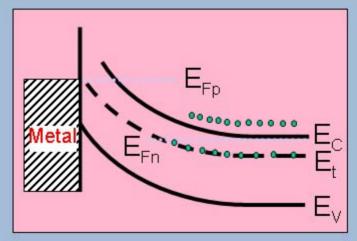
**DLTS** unit

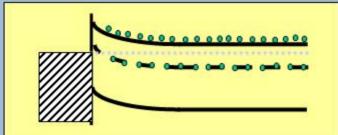
Oscilloscope to view signal

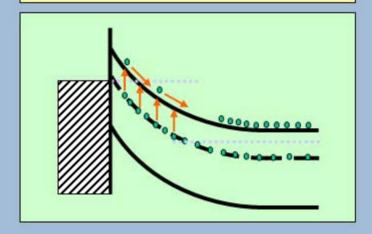
#### The Effects of Traps (Impurities)

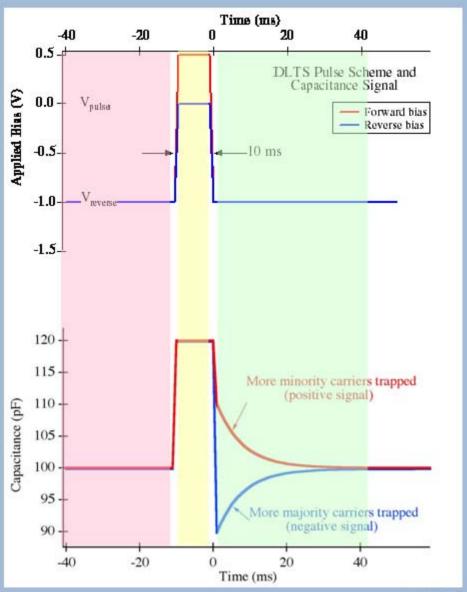


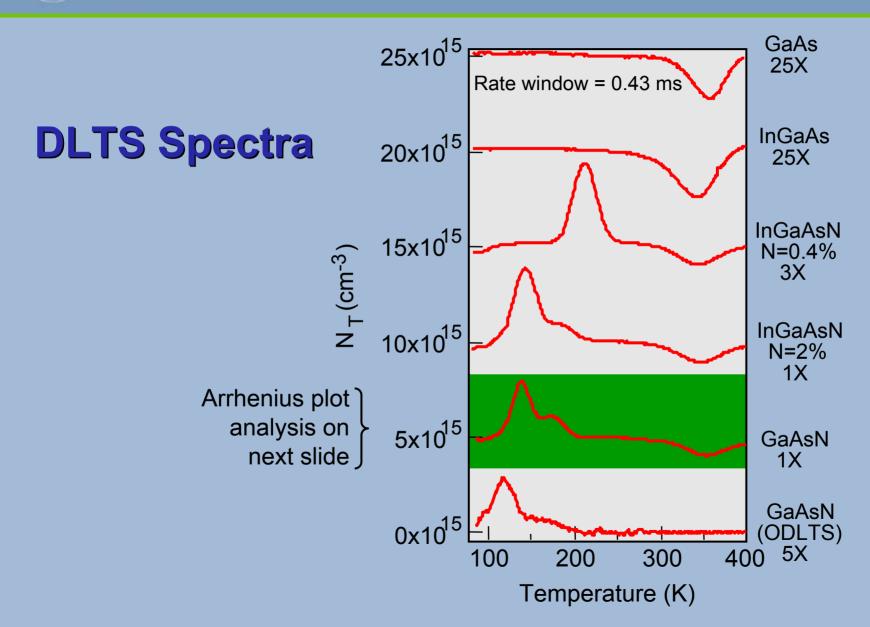
Example: gold in silicon

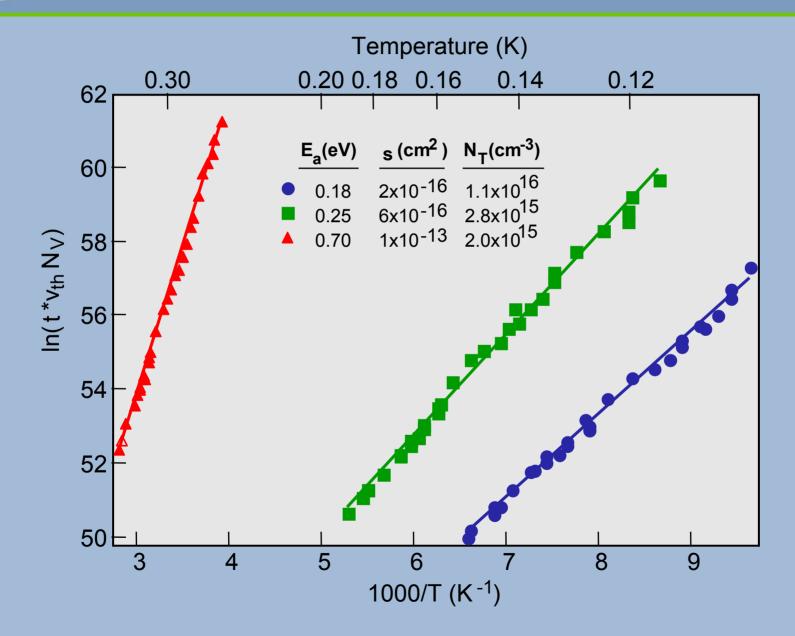




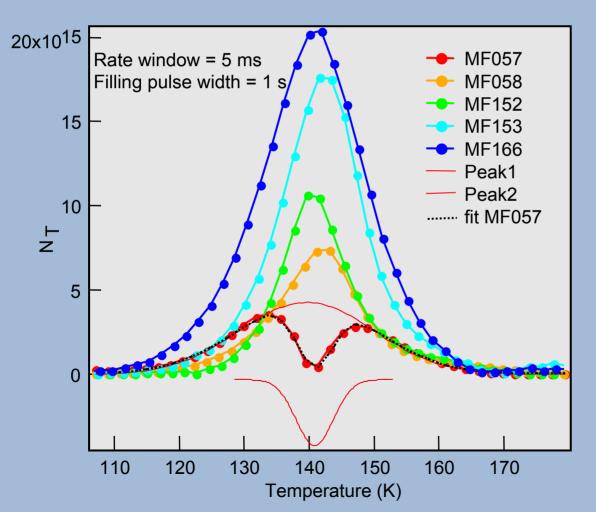






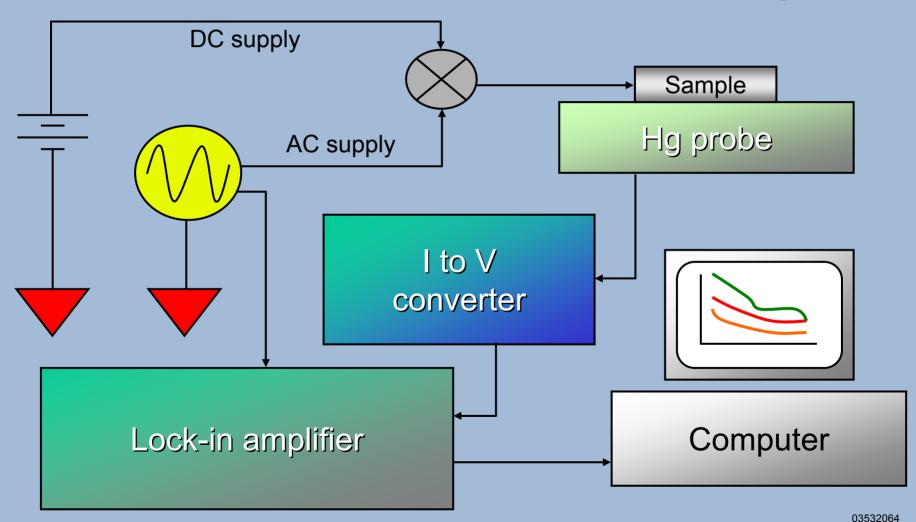


## Concentration of Traps Corresponding with the Low-T Peak Increases with Increasing Amounts of N



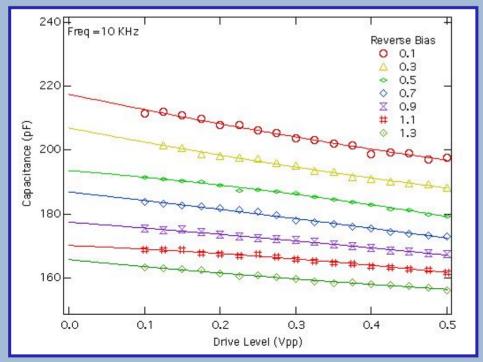
The defect corresponding to this peak may be responsible for the low V<sub>oc</sub> and poor solar cell performance of InGaAsN.

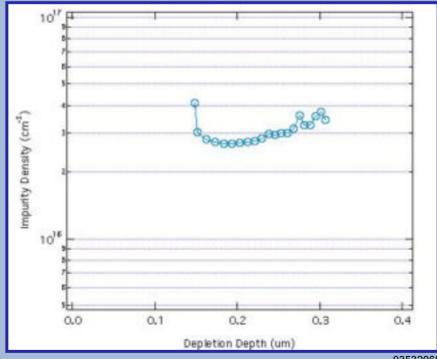
#### **Drive Level Capacitance Profiling**



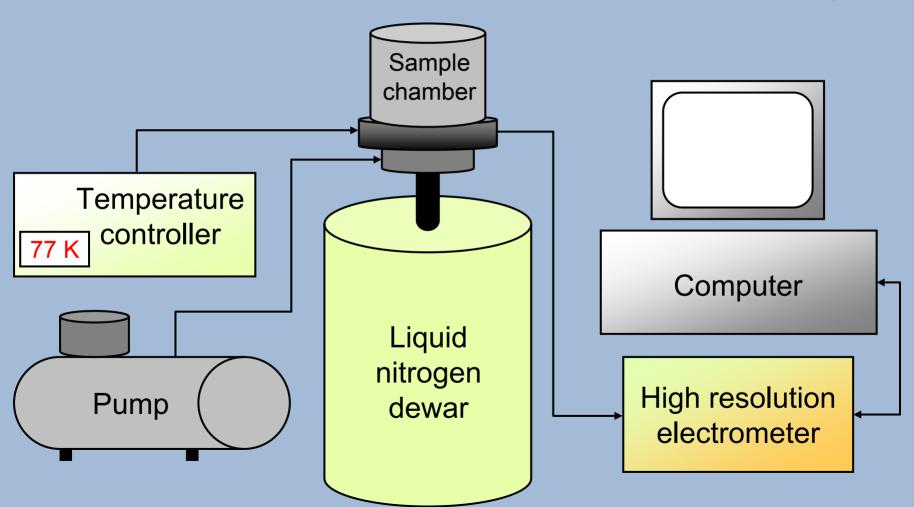
#### Sample Data Using Drive Level **Capacitance Profiling**

Capacitance is plotted versus AC amplitude (plus an adjustment of DC) for several DC biases. Each curve is fit to a 2nd order polynomial to calculate the impurity density at a given depletion depth. This process is repeated for multiple AC frequencies.



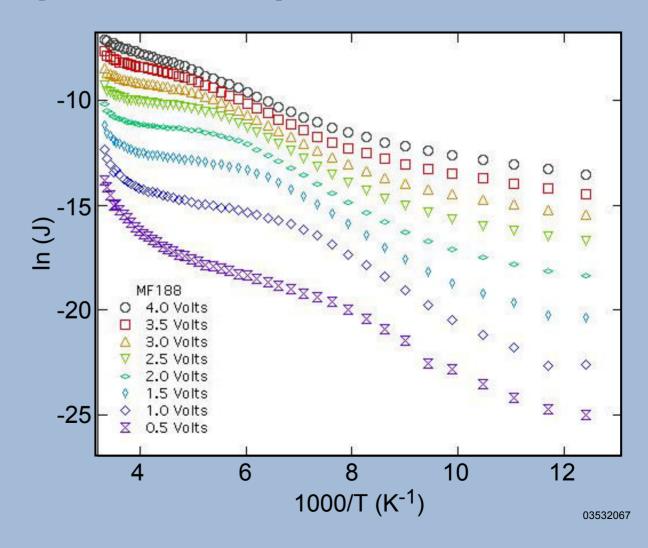


#### **Temperature Dependent Current-Voltage**



#### Sample Temperature Dependent IV Data

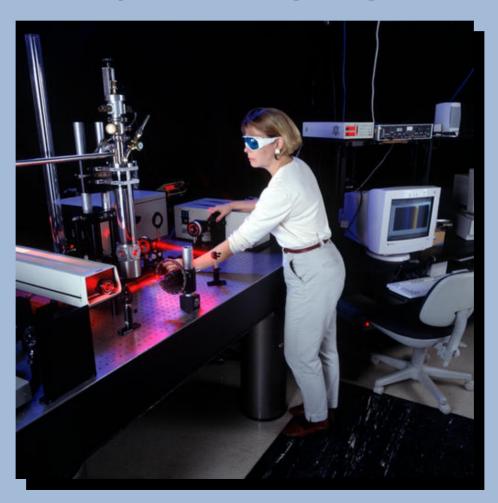
**Current-Voltage** measurements are taken as a sample is cooled down to liquid nitrogen temperature. The current density is then plotted in an Arrhenius plot for several reverse biases. The slope of this data can provide activation energies or insight into band diagram information.



#### **Energy Resolved Photoluminescence**

- Energy resolved photoluminescence is a process that helps researchers determine the bandgap for a semiconductor material and also enables researchers to look for defects within those kinds of materials. The fewer defects a material has the more efficiently it will perform. Photoluminescence is the product of electron hole pairs recombining and producing photons. Those photons are emitted from within the bandgap and below the bandgap if there are defects.
- The experiment set up is very simple and straightforward. Collimated light (laser) is focused onto a semiconductor sample to excite the electrons above the bandgap. Many laser lines can be used. At NREL there are six CW laser lines available and they range between 325nm to 822nm. The photoluminescence is then collected through a lens collimated and focused onto a slit on an imaging spectrograph. This light is then passed through the spectrograph to either a CCD array or a photodiode array. The spectra is then acquired and recorded through data acquisition software onto a computer. It can then be analyzed and archived.

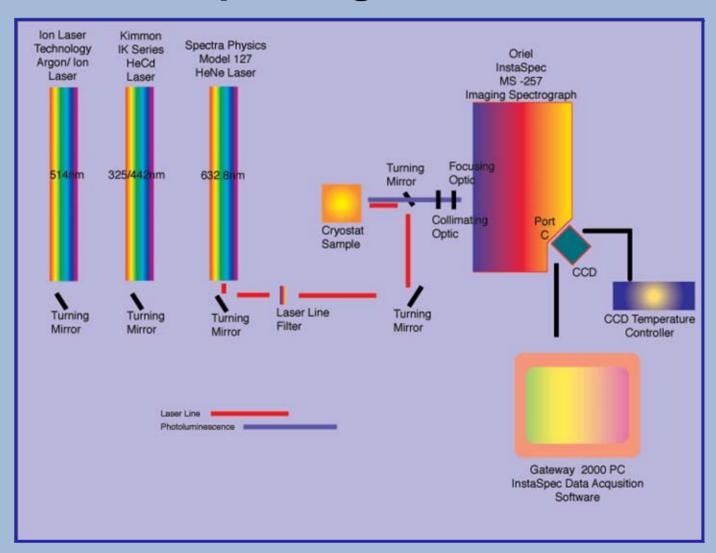
## ERPL Measurement Using Continuous Flow (portable) Cryostat



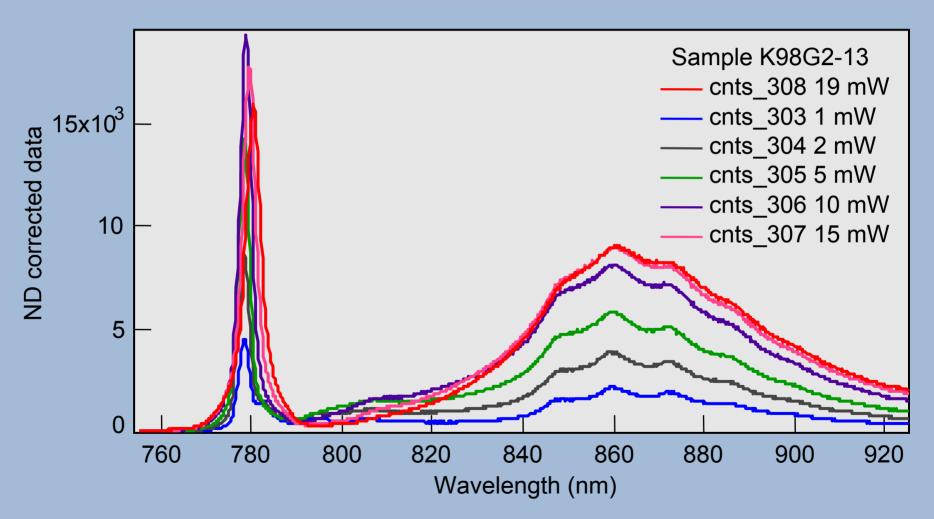
#### **ERPL Capabilities**

- CCD (charge coupled device) Camera for PL measurements in the visible
- InGaAs PDA (photodiode array) for measurements in the NIR
- Imaging spectrometer with four gratings for use with the CCD and PDA
- InSb detector with a scanning monochrometer for measurements in the IR
- Ge detector with a triple grating monochrometer for high resolution measurements at longer wavelengths.
- Closed cycle cryostat which enables measurements to be performed at 4.25 K
- Temperature controller to allow temperature dependent measurements.
- Continuous flow cryostats that are portable and can be used with different setups in the laboratory (technique development)

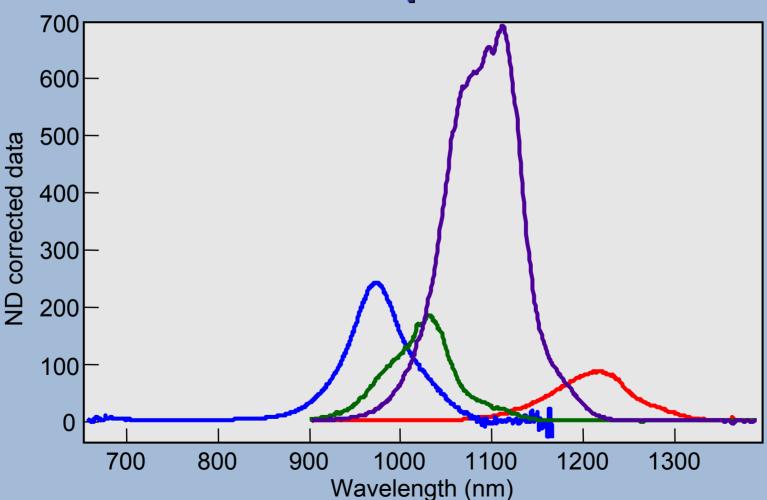
#### **ERPL Setup Using the CCD Camera**



#### PL Spectra of CdTe Film at 4.25K



## PL Spectra of CIS/CGS Material at Room Temperature

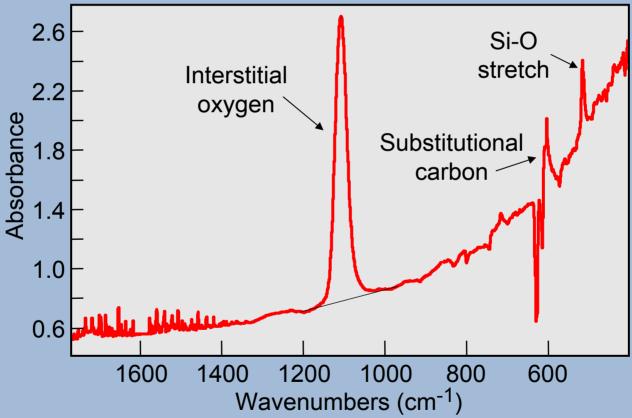




## Fourier Transform Infrared (FTIR) Spectroscopy Brian M. Keyes and Lynn M. Gedvilas

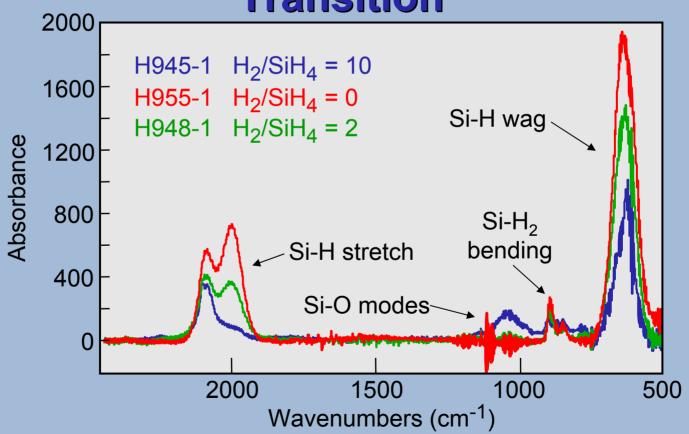
- Reflectance, transmittance, and absorption measurements
- Spectral region is home to molecular and free carrier absorption
- Impurity analysis
- Bonding configurations
- Quantitative analysis
- Nondestructive
- Sensitivity advantages over dispersive systems
- Imaging capabilities
- Low-gap photoluminescence measurements and mapping

#### Impurity Concentrations in Crystalline Silicon



- Impurity content and process control
- Oxygen precipitates related to material quality
- Study of SiN<sub>x</sub> and SiC<sub>x</sub> layers

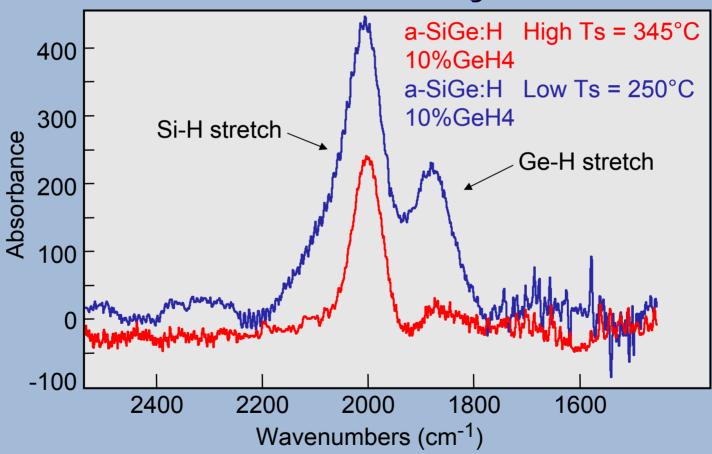
#### Amorphous-Microcrystalline Silicon Transition



- Si-H infrared bonding configurations are related to microcrystallinity
- Higher crystalline volume fractions favor increased oxidation measure of device quality

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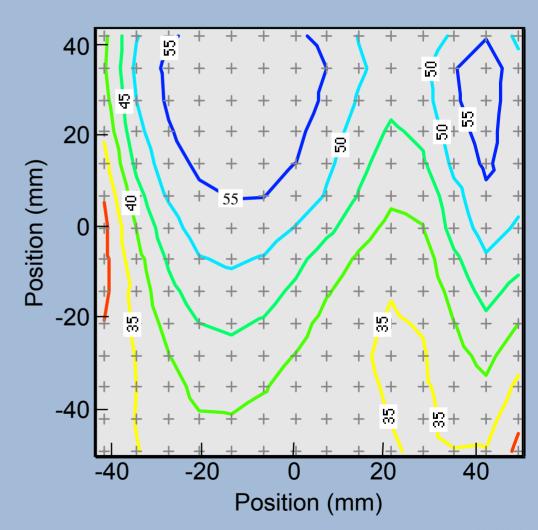
#### a-SiGe:H Alloys



- Low-gap alloy in tandem devices
- Increased Ge-H bonding produces higher-quality alloys

# Transparent Conducting Oxide Films — Uniformity Map

- Support of combinatorial growth efforts
- Reflectance and transmittance maps
- Nondestructive measure of transport properties through determination of plasma frequency

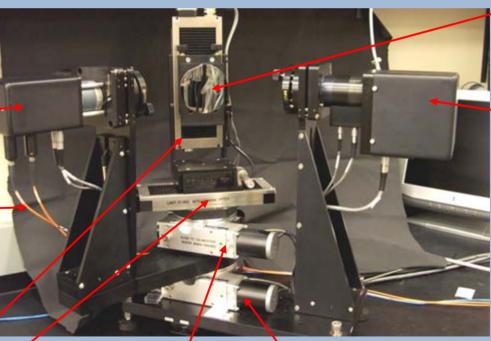


#### Variable Angle Spectroscopic Ellipsometer

- Rotating compensator ellipsometer
- Automated variable angle measurement
- Sample translation and mapping
- Small-spot focusing ability (~1 mm spot size)
- Dual array detectors, 0.7–5.0 eV range, spectra in a few seconds

Collection optics fiber-coupled to remote spectrometers

Optical fibers



Sample (Si wafer)

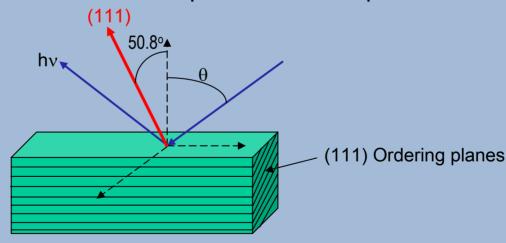
Dual light source
UV – NIR

Sample x, y translation stages

Stepping-motor driven rotation stages — for sample and collection optics arm

#### Optical Properties of Ordered Ga<sub>0.51</sub>In<sub>0.49</sub>P

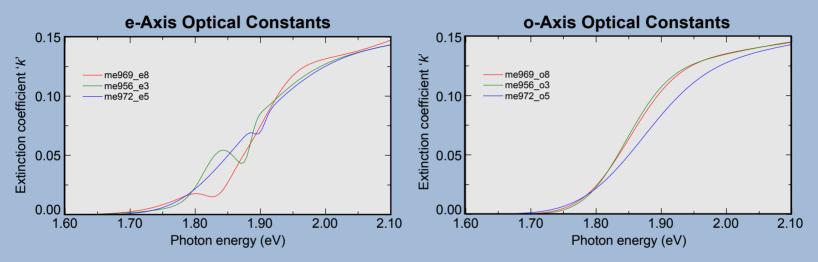
- Ga<sub>0.51</sub>In<sub>0.49</sub>P is a critical component of multi-junction, high efficiency solar cells
- Spontaneous ordering of Ga and In along (111) occurs during MOCVD growth
- Ordering reduces the bandgap and causes optical anisotropy
- These effects depend on the degree of ordering
  - which can be controlled during growth
- Accurate modeling of GaInP-based PV requires accurate optical constants



Crystal cleaves along (110),  $(\overline{1}10)$ ,  $(\overline{1}\overline{1}0)$ , and  $(\overline{1}\overline{1}0)$  planes

• Ellipsometry spectra are measured for various sample orientations to determine anisotropic optical constants

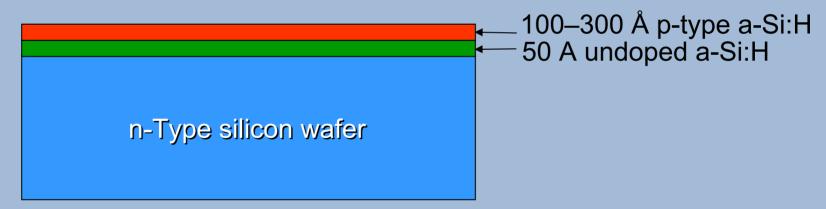
#### Ga<sub>0.51</sub>In<sub>0.49</sub>P Optical Properties vs. Ordering



- Degree of ordering expressed in terms of ordering parameter h, 0 < h < 1</li>
- •In figures above, red corresponds to h = 0.45, green h = 0.31, and blue h = 0.10
- Extraordinary optical constants, on left side, show splitting of valence band max due to reduced symmetry produced by ordering
- Ordinary optical constants on right side shows reduced band gap with ordering

#### In-situ Real Time Spectroscopic Ellipsometry Studies of a-Si:H Growth

- NREL silicon materials team currently working to optimize HIT (heterojunction with intrinsic layer) solar cells
- Devices require very thin amorphous silicon layers on silicon substrate
- Efficiencies as high as 21% have been achieved because of very effective surface passivation by a-Si:H layer on silicon wafer



- Accurate thickness control requires real-time feedback growth rates change with filament aging and changes in deposition gas flow rates, etc.
- Passivation requires immediate a-Si:H deposition at interface epitaxial deposition on wafer surface hinders passivation effect of a-Si:H — c-Si interface

### In-Situ RTSE Provides Real-time Feedback and Post-deposition Analysis of Crystallinity

